Warm Up Problem

What were some of the differences between the symbol table for our assembler and our symbol table for variables?

CS 241 Lecture 16

Type Checking Continued and Code Generation With thanks to Brad Lushman, Troy Vasiga and Kevin Lanctot

Catching Type errors

How does one catch a type error?

- First, figure out the type of every expression using type rules (see next slides/lectures).
- If no such rule exists OR the types do not match a given rule, produce an error.
- Recall: Our ID's types are stored in the ID table.

Inference rules (like in CS 245!)

• If an ID is declared with type τ then it has this type:

$$\frac{\langle \mathsf{id}.\mathsf{name}, \tau \rangle \in \mathsf{declarations}}{\mathsf{id}.\mathsf{name} : \tau}$$

Numbers have type int

NUM: int

NULL is of type int*

NULL: int*

Inference rules (like in CS 245!)

• Parentheses do not change the type

$$\frac{E:\tau}{(E):\tau}$$

• The Address of an int is of type int*

$$\frac{E:int}{\&E:int*}$$

• Dereferencing int* is of type int $\frac{E : int*}{*E : int}$

new int[E]:int*

Arithmetic Operations

$$\frac{E_1: int \quad E_2: int}{E_1*E_2: int}$$

$$\frac{E_1: \mathtt{int} \quad E_2: \mathtt{int}}{E_1/E_2: \mathtt{int}}$$

$$\frac{E_1: int \quad E_2: int}{E_1\%E_2: int}$$

Addition and Subtraction

Addition

$$\frac{E_1 : \text{int} \quad E_2 : \text{int}}{E_1 + E_2 : \text{int}}$$

$$\frac{E_1 : \text{int*} \quad E_2 : \text{int}}{E_1 : \text{int*} \quad E_2 : \text{int*}}$$

$$\frac{E_1 : \text{int} \quad E_2 : \text{int*}}{E_1 : \text{int} \quad E_2 : \text{int*}}$$

Subtraction

$$rac{E_1: ext{int} \quad E_2: ext{int}}{E_1 - E_2: ext{int}} \\ rac{E_1: ext{int} * \quad E_2: ext{int}}{E_1: ext{int} * \quad E_2: ext{int} *} \\ rac{E_1: ext{int} * \quad E_2: ext{int} *}{E_1 - E_2: ext{int}}$$

Procedure Calls:

$$\frac{\langle f, \tau_1, ..., \tau_n \rangle \in \text{declarations} \quad E_1 : \tau_1 \quad E_2 : \tau_2 \quad ... \quad E_n : \tau_n}{f(E_1, ..., E_n) : \text{int}}$$

More on Types

• There is still the issue of control statements, namely:

```
while (T) {S}
if (T) {S1} else {S2}
```

- The value of T above should be a boolean. However WLP4 doesn't have booleans!
- Our grammar however forces T to be a boolean test of type expr comp expr.
- Hence, so long as our exprs are well-typed, the tests will be as well.

Inference Rules For Well-Typed

• Any expression with a type is well-typed $E: \tau$

```
\overline{\text{well-typed}(E): \tau}
```

 Assignment is well-typed if and only if its arguments have the same type

$$\frac{E_1:\tau\quad E_2:\tau}{\mathsf{well-typed}(E_1=E_2)}$$

Print is well-typed if and only if the parameter has type int
 E: int
 E typed(print E)

well-typed(print E)

 Deallocation is well-typed if and only if the parameter has type int*

$$E: \mathtt{int} *$$

well-typed(delete [] E)

Inference Rules For Well-Typed

Comparisons are well-typed if and only if both arguments have the same type (either both int or both int*)

$$\begin{array}{ll} \underline{E_1:\tau\quad E_2:\tau} & \underline{E_1:\tau\quad E_2:\tau} \\ \text{well-typed}(E_1 < E_2) & \overline{\text{well-typed}(E_1 < = E_2)} \\ \\ \underline{E_1:\tau\quad E_2:\tau} & \underline{E_1:\tau\quad E_2:\tau} \\ \text{well-typed}(E_1 > E_2) & \overline{\text{well-typed}(E_1 > = E_2)} \\ \\ \underline{E_1:\tau\quad E_2:\tau} & \underline{E_1:\tau\quad E_2:\tau} \\ \text{well-typed}(E_1 = = E_2) & \overline{\text{well-typed}(E_1! = E_2)} \end{array}$$

Statements

- $\frac{\bullet \ \, \mathsf{The} \ \mathsf{empty} \ \mathsf{sequence} \ \mathsf{is} \ \mathsf{well-typed}}{\mathsf{well-typed}(\)}$
- Consecutive statements are well-typed if and only if each statement is well-typed $\frac{\text{well-typed}(S_1) \quad \text{well-typed}(S_2)}{\text{well-typed}(S_1; S_2)}$

Procedures

 Procedures are well-typed if and only if the body is well-typed and the procedure returns an int: well-typed(S) E: int

```
\overline{\text{well-typed(int } f(\text{dcl}_1,...,\text{dcl}_n)\{\text{dcls } S \text{ return } E;)\}}
```

 Wain is also well-typed but requires the following precise signature:

```
dcl_2 = int id well-typed(S) E: int
```

 $\overline{\text{well-typed(int } f(dcl_1, dcl_2)\{dcls S \text{ return } E;)\}}$

Notice that the first declaration can be an int or int*.

Control Statements

 All of the parts of an if statement are well-typed if and and only if the if statement itself
 well-typed(T) well-typed(S₁) well-typed(S₂)

```
well-typed(if (T) \{S_1\} else \{S_2\})
```

• All of the parts of a while statement are well-typed if and and only if the while statement itself well-typed(T) well-typed(S)

 $well-typed(while (T) \{S\})$

Assignments

There is a final sanity check with the left and right hand sides of an assignment statement.

- Given an expression, say x=y, notice that the left hand side and the right hand side represent different things
- The left hand side represents a place to store data; it must be a location of memory (think of variables as being memory location containers)
- The right hand side must be a value; that is, any well-typed expression.
- Anything that denotes a storage location we shall call an lvalue.

Example

Consider the following two snippets of code:

```
int x = 0;
x = 5;
int x = 0;
5 = x;
```

This is okay; the *Ivalue* x is a storage location

This is **not** okay; the *Ivalue* 5 is an integer and not a storage location.

For us, *Ivalues* are any of variable names, dereferenced pointers and any parenthetical combinations of these. These are all forced on us by the WLP4 grammar so the checking is done for you.

Assignment Overview:

- A6: WLP4 Text File to WLP4 Tokens and lexemes (Lexical Analysis)
- A7: WLP4 Tokens and lexemes to parse tree (Syntactic Analysis)
- A8 Parse Trees to Augmented Parse Tree and Symbol Tables (Context-Sensitive Analysis)
- A9 and 10: Augmented Parse Trees to MIPS Assembly Language (Code Generation)

We are now on the final chapter of our journey; taking WLP4 code and converting it into MIPS assembly language.

Code Generation

- By the time we reach A9 and A10, we can assume our code has no compiler errors; that is, it is semantically and syntactically correct (no syntax, type, scoping or variable errors).
- We want to take such code and write an equivalent program in MIPS. This is our final step.
- Combined with the fact that we wrote code to take MIPS into binary; we have completed our full compiler.

Food For Thought

There are infinitely many equivalent MIPS programs to a single WLP4 program. Which should we output?

- Correctness is most important!
- For us, we seek a simplistic solution (we don't want to overcomplicate this if we can avoid it)
- Efficiency to compile (something that is exponential in the number of lines of code is likely not useful)
- Efficiency of the code itself (that is, how fast does it run?)
 For us, we will use number of lines as our measure of efficiency (yes this is not great but it is an easy metric!)

First Example

```
Let's try the following:
```

```
int wain(int a, int b){
  return a;
}
```

Recall our mips.twoints convention that registers 1 and 2 store the input values and register 3 returns the values. We need to put register 1 inside our output register \$3. What MIPS command does this?

Solution

add \$3, \$1, \$0

Almost done! What's missing?

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```
add $3, $1, $0
Almost done! What's missing?
add $3, $1, $0
jr $31
There's one hiccup...
```

A small concern

What is a corresponding MIPS program for this WLP4 program?

```
int wain(int a, int b){
  return b;
}
```

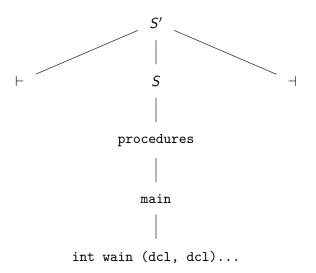
A small concern

What is a corresponding MIPS program for this WLP4 program?

```
int wain(int a, int b){
  return b;
}
Answer:
add $3, $2, $0
jr $31
```

Seems fine but the issue is that both functions have the same parse tree!

Parse Tree



and so on.

The Main Issue

- The parse tree isn't going to be enough to determine the difference between the two pieces of code.
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Symbol	Type	Location
a	int	\$1
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... by now however you know nothing is ever this easy. What can go wrong?

Issues

- Storing variables and parameters in registers will force us to run out of registers quickly (Think about recursive code!)
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- Storing variables and parameters in registers will force us to run out of registers quickly (Think about recursive code!)
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- Store them on the stack!

Code Generation

```
int wain(int a, int b){ return a;}
Becomes
sw $1, -4($30)
sw $2, -8($30)
lis $4
.word 4
sub $30, $30, $4
sub $30, $30, $4
lw $3. 4($30)
add $30, $30, $4
add $30, $30, $4
jr $31
```

We make the convention that \$4 always contains the number 4.

Updates to Symbol Table

Instead of the symbol table storing registers, it should actually store the offset from the stack pointer!

Symbol	Туре	Offset (from \$30)
a	int	4
Ъ	int	0

Unfortunately, this also gives us problems - why?

New Variables

Variables also have to go on the stack but we don't know what the offsets should be until we process all of the variables and parameters! For example,

```
int wain(int a, int b){ int c = 0; return a;}
```

Let's generate the code for this program (see next slide)

Code Generated

```
int wain(int a, int b){ int c = 0; return a;}
sw $1, -4($30)
sw $2. -8($30)
lis $4
.word 4
sub $30, $30, $4
sub $30, $30, $4
sub $30, $30, $4 ; For int c
sw $0, 0($30)
                 ; c = 0
lw $3, 8($30)
                    ;8 from the symbol table
add $30, $30, $4
add $30, $30, $4
add $30, $30, $4
jr $31
```

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```
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Symbol	Туре	Offset (from \$30)
a	int	8
Ъ	int	4
С	int	0

As we process the code, we need to be able to compute the offset as we see the values. Also, we need to handle intermediate values of complicated arithmetic expressions by storing on the stack! We cannot do this from \$30.

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Remember that the key issue here is that \$30, the top of stack frame **changes**. Reference the offset from the **bottom** of the stack frame! This is the value we will store in \$29 (Please note that often times \$30 represents this value in standard MIPS conventions).

If we calculate offsets from \$29, then no matter how far we move the top of the stack, the offsets from \$29 will be unchanged!

Code Generated

```
int wain(int a, int b){ int c = 0; return a;}
lis $4
.word 4
sub $29, $30, $4
sw $1. -4($30)
sub $30, $30, $4
sw $2. -4($30)
sub $30, $30, $4
sw $0, -4($30)
                      ; For int c = 0
sub $30, $30, $4
lw $3, 0($29)
                    ;Offset in symbol table
add $30, $30, $4
add $30, $30, $4
add $30, $30, $4
ir $31
```

New Variables with \$29

int wain(int a, int b){ int c = 0; return a;}

Symbol	Туре	Offset (from \$29)
a	int	0
Ъ	int	-4
С	int	-8

This is easier with \$29.

Something Harder

What about a more complicated program:

```
int wain(int a, int b){
  return a-b;
}
```

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Well, we already have the convention that \$3 stores the output so let's use this for scratch work in between.

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Use the convention that \$5 also stores intermediate work!

More Code

```
lis $4
.word 4
sub $29, $30, $4
sw $1, -4($30)
sub $30, $30, $4
sw $2, -4($30)
sub $30, $30, $4
lw $3, 0($29)
add $5, $3, $0
lw $3, -4($29)
sub $3, $5, $3
add $30, $30, $4
add $30, $30, $4
ir $31
```

```
;Offset in symbol table (a);Store a in $5;Offset in symbol table (b)
```

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Consider adding something like a + (b - c). Would need to load a, load b, load c compute b - c, then compute the answer. This would require a third register. (Remember, we want to process code scanning left to right).

Where should we store these values instead?

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Consider adding something like a + (b - c). Would need to load a, load b, load c compute b - c, then compute the answer. This would require a third register. (Remember, we want to process code scanning left to right).

Where should we store these values instead? On the stack again! This way we only will ever need two registers for scratch work!

Simplicity

We will use some short hands for our code. Define code(a) by 1w \$3, N(\$29) where N is the offset in the symbol table. Further, define push(\$3) by

```
sw $3, -4($30)
sub $30, $30, $4
and pop($5) by
add $30, $30, $4
lw $5, -4($30)
```

Try to compute the code for a + (b - c) using these commands.