

ALEJANDRO SALINGER

Citizenships: Chilean, German

ajsalinger@cs.uwaterloo.ca

<http://www.cs.uwaterloo.ca/~ajsaling>

CURRENT POSITION

- **Software Engineer in R&D**, October 2014 - present
SAP SE, Walldorf, Germany

POSTDOCTORAL RESEARCH EXPERIENCE

- **Postdoctoral Researcher**, July 2013 - September 2014
Saarland University, Saarbrücken, Germany
Supervisor: Dr. Reinhard Wilhelm
- **Postdoctoral Fellow**, May 2013 - June 2013
University of Waterloo, Waterloo, Ontario, Canada
Supervisor: Dr. Alejandro López-Ortiz

EDUCATION

- **Ph.D. Computer Science**, 2013
University of Waterloo, Waterloo, Ontario, Canada
Supervisors: Dr. Alejandro López-Ortiz, Dr. Ian Munro
Thesis title: Models for Parallel Computation in Multi-Core, Heterogeneous, and Ultra Wide-Word Architectures
- **Certificate in University Teaching**, 2012
Centre for Teaching Excellence, University of Waterloo, Waterloo, Ontario, Canada
- **Engineering in Computer Science**, 2005
B.Sc. in Engineering Sciences, Computer Science
University of Chile, Santiago, Chile
Supervisor: Dr. Gonzalo Navarro
Undergraduate thesis title: A Simple Alphabet-Independent Compressed Index
- **Education Abroad Program**, 2003 - 2004
University of California, San Diego, La Jolla, California, USA

RESEARCH INTERESTS

Algorithms and data structures, parallel computation, online algorithms, real-time systems, cache eviction policies, computational geometry, text indexing, information retrieval.

HONORS AND AWARDS

- David R. Cheriton Graduate Scholarship in Computer Science, University of Waterloo (CAD 10,000/year) (2009-2010)
 - David R. Cheriton Graduate Scholarship in Computer Science, University of Waterloo (CAD 10,000/year) (2007-2009)
 - Marcos Orrego Puelma award, granted by the Institute of Engineers of Chile to the best student to graduate each year from the School of Engineering and Sciences of the University of Chile (2006)
 - University of Waterloo Graduate Entrance Scholarship (CAD 4,000) (2006)
 - Ranked 1st out of the 545 students in the School of Engineering and Sciences that entered in 1999, University of Chile (2005)
 - Ranked 1st, Department of Computer Science, University of Chile (2002-2004)
 - Honors List, School of Engineering and Sciences (top 6%) (1999-2002, 2004)
 - Provosts Honors, University of California, San Diego (2003)
 - Academic Excellence Scholarship for entering 3rd out of 545 to the School of Engineering and Sciences, University of Chile (1st year tuition exemption) (1999)
 - Irma Salas Scholarship for obtaining the top score in the nationwide university entrance examination in Mathematics, University of Chile (1999)
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PUBLICATIONS

Book Chapters

1. Alejandro López-Ortiz and Alejandro Salinger
Analyzing Cache Behaviour in Multicore Architectures
Encyclopedia of Algorithms, 2015
2. Ricardo Baeza-Yates and Alejandro Salinger
Fast Intersection Algorithms for Sorted Sequences (extended version of 23)
Algorithms and Applications 2010, 45-61, 2010

Journal Publications

3. Reza Dorrigiv, Stephane Durocher, Arash Farzan, Robert Fraser, Alejandro López-Ortiz, J. Ian Munro, Alejandro Salinger, and Matthew Skala
The Hausdorff Core Problem on Simple Polygons (extended version of 20)
Journal of Computational Geometry (JoCG) 5(1):14-40, 2014.
4. Alejandro López-Ortiz, Alejandro Salinger, and Robert Suderman
Toward a Generic Hybrid CPU-GPU Parallelization of Divide-and-Conquer Algorithms (extended version of 13)
International Journal of Networking and Computing (IJNC) 4(1):131-150, 2014

5. Diego Arroyuelo, Francisco Claude, Reza Dorrigiv, Stephane Durocher, Meng He, Alejandro López-Ortiz, J. Ian Munro, Patrick Nicholson, Alejandro Salinger, and Matthew Skala
Untangled Monotonic Chains and Adaptive Range Search
Theoretical Computer Science (TCS) 412(32):4200 - 4211, 2011
6. Francisco Claude, Gautam K. Das, Reza Dorrigiv, Stephane Durocher, Robert Fraser, Alejandro López-Ortiz, Bradford G. Nickerson, and Alejandro Salinger
An Improved Line-Separable Algorithm for Discrete Unit Disk Cover
Discrete Mathematics, Algorithms and Applications (DMAA), 2(1), pp. 77-87, 2010
7. Jérémy Barbay, Alejandro López-Ortiz, Tyler Lu, and Alejandro Salinger
An Experimental Investigation of Set Intersection Algorithms for Text Searching
Journal of Experimental Algorithmics (JEA), 14: 3.7–3.24, 2009
8. Szymon Grabowski, Gonzalo Navarro, Rafal Przywarski, Alejandro Salinger, and Veli Mäkinen
A Simple Alphabet-Independent FM-Index (extended version of 24)
International Journal of Foundations of Computer Science (IJFCS), 17(6):1365-1384, 2006
9. Maxime Crochemore, Costas Iliopoulos, Gonzalo Navarro, Yoan Pinzon, and Alejandro Salinger
Bit-parallel (delta,gamma)-Matching Suffix Automata
Journal of Discrete Algorithms (JDA) 3(2-4):198-214, 2005

Conference Publications

10. Jan Reineke and Alejandro Salinger
On the Smoothness of Paging Algorithms
In *Proc. of the 13th Workshop on Approximation and Online Algorithms (WAOA)*, 17-18 September, Patras, Greece, 2015
11. Arash Farzan, Alejandro López-Ortiz, Patrick K. Nicholson, and Alejandro Salinger
Algorithms in the Ultra-Wide Word Model
In *Proc. of the 12th Annual Conference on Theory and Applications of Models of Computation (TAMC)*, pp. 335-346, Singapore, May 18-20, 2015
12. Alejandro López-Ortiz and Alejandro Salinger
On the Sublinear Processor Gap for Parallel Architectures
In *Proc. of the 10th Annual Conference on Theory and Applications of Models of Computation (TAMC)*, pp. 193-204, Hong Kong, China, May 20-22, 2013
13. Alejandro López-Ortiz, Alejandro Salinger, and Robert Suderman
Toward a Generic Hybrid CPU-GPU Parallelization of Divide-and-Conquer Algorithms
In *IEEE 27th International Parallel and Distributed Processing Symposium Workshops & PhD Forum (IPDPSW)*, pp. 601-610, 2013
14. Francisco Claude, Reza Dorrigiv, Shahin Kamali, Alejandro López-Ortiz, Pawel Pralat, Jazmín Romero, Alejandro Salinger, and Diego Seco
Broadcasting in Conflict-Aware Multi-Channel Networks
In *Proc. of the 7th Workshop on Algorithms and Computation (WALCOM)*, pp. 158-169, Kharagpur, India, February 14-16, 2013
15. Alejandro López-Ortiz and Alejandro Salinger
Minimizing Cache Usage in Paging
In *Proc. of the 10th Workshop on Approximation and Online Algorithms (WAOA)*, pages 145-158, Ljubljana, Slovenia, September 13-14, 2012
16. Alejandro López-Ortiz and Alejandro Salinger
Paging for Multi-Core Shared Caches
In *Proc. of the 3rd Innovations in Theoretical Computer Science (ITCS)*, pp. 113-127, Cambridge, MA, USA, January 8-10, 2012

17. Alejandro López-Ortiz and Alejandro Salinger
Brief Announcement: Paging for Multicore Processors
In Proc. of the 23rd Annual ACM Symposium on Parallel Algorithms and Architectures (SPAA), pp. 137-138, San Jose, CA, USA, June 4-6, 2011
18. Francisco Claude, Reza Dorrigiv, Stephane Durocher, Robert Fraser, Alejandro López-Ortiz, and Alejandro Salinger
Practical Discrete Unit Disk Cover Using an Exact Line-Separable Algorithm
In Proc. of the 20th International Symposium on Algorithms and Computation (ISAAC), LNCS 5878, pp. 45-54, 2009.
19. Diego Arroyuelo, Francisco Claude, Reza Dorrigiv, Stephane Durocher, Meng He, Alejandro López-Ortiz, J. Ian Munro, Patrick Nicholson, Alejandro Salinger, and Matthew Skala
Untangled Monotonic Chains and Adaptive Range Search
In Proc. 20th International Symposium on Algorithms and Computation (ISAAC), LNCS 5878, pp. 203-212, Honolulu, Hawaii, USA, December 2009
20. Reza Dorrigiv, Stephane Durocher, Arash Farzan, Robert Fraser, Alejandro López-Ortiz, J. Ian Munro, Alejandro Salinger, and Matthew Skala
Finding a Hausdorff Core of a Polygon: On Convex Polygon Containment with Bounded Hausdorff Distance
In Proc. of the Algorithms and Data Structures Symposium (WADS), LNCS 5664, pp. 218-229, 2009.
21. Reza Dorrigiv, Alejandro López-Ortiz, and Alejandro Salinger
Optimal Speedup on a Low-Degree Multi-Core Parallel Architecture (LoPRAM)
In Proc. of the 20th ACM Symposium on Parallelism in Algorithms and Architectures (SPAA), pp. 185-187, Munich, Germany, June 2008
22. Rafal Przywarski, Szymon Grabowski, Gonzalo Navarro, and Alejandro Salinger
FM-KZ: An Even Simpler Alphabet-Independent FM-Index
In Proc. Prague Stringology Conference (PSC), pp. 226-240, 2006
23. Ricardo Baeza-Yates and Alejandro Salinger
Experimental Analysis of a Fast Intersection Algorithm for Sorted Sequences
In Proc. String Processing and Information Retrieval (SPIRE), Springer LNCS 3772, pp. 13-24, Buenos Aires, Argentina, November 2005
24. Szymon Grabowski, Veli Mäkinen, Gonzalo Navarro, and Alejandro Salinger
A Simple Alphabet-Independent FM-Index
In Proc. Prague Stringology Conference (PSC), pp. 230-244, 2005

Posters

1. Alejandro López-Ortiz and Alejandro Salinger
Minimizing Cache Usage in Paging
10th Annual Conference on Theory and Applications of Models of Computation (TAMC), Hong Kong, China, May 2013
2. Alejandro López-Ortiz and Alejandro Salinger
Paging for Multicore (CMP) Caches
Cheriton Research Symposium, David R. Cheriton School of Computer Science, University of Waterloo, September 2010
3. Reza Dorrigiv, Alejandro López-Ortiz, and Alejandro Salinger
Optimal Speedup on a Low-Degree Multi-Core Parallel Architecture (LoPRAM)
20th ACM Symposium on Parallelism in Algorithms and Architectures (SPAA), Munich, Germany, June 2008

4. Robert Fraser, Alejandro Salinger, Reza Dorriv, Joseph Horton, and Alejandro López-Ortiz
Defensive Strategies for a Soccer Goalkeeper with a Single Adversary.
18th Annual Canadian Conference on Intelligent Systems (IS), Windsor, Ontario, Canada, May 2008

Technical Reports

5. Francisco Claude, Reza Dorriv, Shahin Kamali, Alejandro López-Ortiz, Pawel Pralat, Jazmín Romero, Alejandro Salinger, and Diego Seco
Broadcasting in Conflict-Aware Multi-Channel Networks
Technical Report CS-2012-22, David R. Cheriton School of Computer Science, University of Waterloo, 2012
6. Arash Farzan, Alejandro López-Ortiz, Patrick K. Nicholson, and Alejandro Salinger
Algorithms in the Ultra-Wide Word Model
Technical Report CS-2012-21, David R. Cheriton School of Computer Science, University of Waterloo, 2012
7. Alejandro López-Ortiz and Alejandro Salinger
On the Sublinear Processor Gap for Multi-Core Architectures
Technical Report CS-2012-20, David R. Cheriton School of Computer Science, University of Waterloo, 2012
8. Alejandro López-Ortiz and Alejandro Salinger
Minimizing Cache Usage in Paging
Technical Report CS-2012-15, David R. Cheriton School of Computer Science, University of Waterloo, 2012
9. Alejandro López-Ortiz and Alejandro Salinger
Paging for Multicore Processors
Technical Report CS-2011-12, David R. Cheriton School of Computer Science, University of Waterloo, 2011
10. Alejandro López-Ortiz and Alejandro Salinger
Paging for Multicore (CMP) Caches
Technical Report CS-2010-15, David R. Cheriton School of Computer Science, University of Waterloo, 2010
11. Reza Dorriv, Alejandro López-Ortiz, and Alejandro Salinger
Optimal Speedup on a Low-Degree Multi-Core Parallel Architecture (LoPRAM)
Technical Report CS-2007-48, David R. Cheriton School of Computer Science, University of Waterloo, 2007
12. Jérémy Barbay, Alejandro López-Ortiz, Tyler Lu, and Alejandro Salinger
Faster Set Intersection Algorithms for Text Searching
Technical Report CS-2007-13, David R. Cheriton School of Computer Science, University of Waterloo, 2007

Theses

- Alejandro Salinger
Models for Parallel Computation in Multi-Core, Heterogeneous, and Ultra Wide-Word Architectures
Ph.D. thesis, University of Waterloo, 2013
- Alejandro Salinger
A Simple Alphabet-Independent Compressed Index (In Spanish)
Undergraduate thesis, University of Chile, 2005

TALKS

Conference and Workshop Talks

1. On the Robustness of Paging Algorithms. Automatic Verification And Analysis of Complex Systems (AVACS)
Plenary Workshop, Saarbrücken, Germany, September 2014
2. Robustness of Cache Eviction Policies. Automatic Verification And Analysis of Complex Systems (AVACS)
Plenary Workshop, Freiburg, Germany, October 2013

3. On the Sublinear Processor Gap for Parallel Architectures. Theory and Applications of Models of Computation (TAMC), Hong Kong, China, May 2013
4. Minimizing Cache Usage in Paging. Workshop on Approximation and Online Algorithms (WAOA), Ljubljana, Slovenia, September 2012
5. Short vs. Extended Answer Questions in Computer Science Exams. Opportunities and New Directions: Sharing Practices and Research on Teaching and Learning, Waterloo, Ontario, Canada, April 2012
6. Paging for Multi-Core Shared Caches. Innovations in Theoretical Computer Science (ITCS), Cambridge, Massachusetts, USA, January 2012
7. Paging for Multicore Processors. ACM Symposium on Parallel Algorithms and Architectures (SPAA), San Jose, California, USA, June 2011
8. Paging for Multicore Processors. University of Waterloo Graduate Student Research Conference 2011, Waterloo, Ontario, Canada, April 2011
9. Theoretical Modeling of Multicore Computation. DIMACS Workshop on Parallelism: A 2020 Vision, Piscataway, New Jersey, USA, February 2011
10. Practical Discrete Unit Disk Cover Using an Exact Line-Separable Algorithm. Workshop on Optimal Data Structures for Efficient Organization and Retrieval of Massive Spatial Data, Fredericton, New Brunswick, Canada, June 2010
11. Optimal Speedup on a Low-Degree Multi-Core Parallel Architecture (LoPRAM). Workshop on Theory & Many-Cores (T&MC), College Park, Maryland, USA, May 2009
12. Optimal Speedup on a Low-Degree Multi-Core Parallel Architecture (LoPRAM). ACM Symposium on Parallelism in Algorithms and Architectures (SPAA), Munich, Germany, June 2008
13. Optimal Speedup on a Low-Degree Multi-Core Parallel Architecture (LoPRAM). SHARCNET Symposium on GPU and CELL computing, Waterloo, Ontario, Canada, May 2008

Seminar Talks

14. Towards a Web-scale Data Management Ecosystem Demonstrated by SAP HANA. Dagstuhl Seminar: Data Structures and Advanced Models of Computation on Big Data, Wadern, Germany, March 2016.
15. Algorithms in the Ultra-Wide Word Model. Dagstuhl Seminar: Data Structures and Advanced Models of Computation on Big Data, Wadern, Germany, February 2014.
16. Algorithms in the Ultra-Wide Word Model. Algorithms and Complexity Seminar, Laboratoire d'Informatique Algorithmique: Fondements et Applications (LIAFA), Paris, France, January 2014
17. Algorithms in the Ultra-Wide Word Model. Institute of Theoretical Informatics, Karlsruhe Institute of Technology, Karlsruhe, Germany, October 2013
18. Minimizing Cache Usage in Paging. Algorithms and Complexity Seminar, David R. Cheriton School of Computer Science, University of Waterloo, Waterloo, Ontario, Canada, August 2012
19. Paging for Multi-Core Shared Caches. Institute of Theoretical Informatics, Karlsruhe Institute of Technology, Karlsruhe, Germany, November 2011
20. Practical Discrete Unit Disk Cover Using an Exact Line-Separable Algorithm. Discrete Mathematics Seminar. Department of Mathematical Engineering, University of Chile, Santiago, Chile, December 2010
21. Parallel Computation in Multicore Architectures. International Olympiad in Informatics, Waterloo, Ontario, Canada, August 2010

22. Design and Analysis of Algorithms for Multicore Architectures. Max-Planck-Institut für Informatik, Saarbrücken, Germany, April 2009
 23. Design and Analysis of Algorithms for Multicore Architectures. Algorithms and Complexity Seminar, David R. Cheriton School of Computer Science, University of Waterloo, Waterloo, Ontario, Canada, July 2008
 24. Design and Analysis of Algorithms for Multicore Architectures. Department of Computer Science, University of Chile, Santiago, Chile, April 2008
 25. Experimental Analysis of a Fast Intersection Algorithm for Sorted Sequences. Algorithms and Complexity Seminar, David R. Cheriton School of Computer Science, University of Waterloo, Waterloo, Ontario, Canada, May 2006
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RESEARCH VISITS

- Institute for Computer Science, Goethe University Frankfurt am Main, Frankfurt, Germany, March 2014
 - Laboratoire d'Informatique Algorithmique: Fondements et Applications (LIAFA), Paris, France, January 2014
 - Institute of Theoretical Informatics, Karlsruhe Institute of Technology, Karlsruhe, Germany, November 2011, October 2013
 - Max-Planck-Institut für Informatik, Saarbrücken, Germany, April 2009
 - Caesarea Edmond Benjamin de Rothschild Foundation Institute for Interdisciplinary Applications of Computer Science (CRI), University of Haifa, Haifa, Israel, March 2007
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RESEARCH EXPERIENCE

- **Research Assistant**, May 2006 to April 2013
Algorithms and Complexity Group
David R. Cheriton School of Computer Science, University of Waterloo
Supervised by Profs. Alejandro López-Ortiz and Ian Munro
 - Parallel computation, Online algorithms, Intersection algorithms for Information Retrieval, Data structures for spatial data
- **Undergraduate Research Assistant**, March 2004 to April 2005
Department of Computer Science, University of Chile
Supervised by Prof. Gonzalo Navarro
 - Compressed text indices
- **Undergraduate Research Assistant**, March to August 2004
Department of Computer Science, University of Chile
Supervised by Prof. Ricardo Baeza-Yates
 - Intersection algorithms for Information Retrieval
- **Undergraduate Research Assistant**, March to August 2003
Department of Computer Science, University of Chile
Supervised by Prof. Gonzalo Navarro
 - Approximate text searching

WORK EXPERIENCE

- **Software developer**, December 2005 to April 2006
Blue Company, Santiago, Chile
 - Developed a web platform for publishing blogs and webpages (www.bligoo.com)
 - Technologies: J2EE, Hibernate, AJAX, SQL
- **Consultant (Processes and Information Technology)**, April to September 2005
Deloitte, Santiago, Chile
 - Worked in reengineering project for the public owned audit authority in Chile
 - Specified redesigned processes in use cases
 - Identified requirements and designed software models (database, class diagram) for information systems
 - Analyzed and recommended Commercial Off-The-Shelf software to support processes
 - Technologies: UML
- **Project manager** (Software Project Course, U. of Chile), March to July 2004 (part time)
GS1 Chile, National Chamber of Commerce, Santiago, Chile
 - Successfully led a team of 7 students in the design and development of a web system for bar code registration
 - Experience in project leadership and resource management
 - Technologies: C#
- **Software developer**, March 2002 to September 2003 (part time)
IT Development Area (Area de Desarrollo de Infotecnologías, ADI), School of Engineering and Sciences, University of Chile, Santiago, Chile
 - Developed SMS notification system for students and web application for course enrollment
 - Technologies: PHP, SQL, JavaScript

TEACHING EXPERIENCE

- **Adjunct Lecturer**
School of Computer Science, University of Waterloo
 - CS136 - Elementary Algorithm Design and Data Abstraction (Winter 2013)
 - **Guest Lectures**
School of Computer Science, University of Waterloo
 - CS240 - Data Structures and Data Management (Spring 2009, Winter 2012)
 - CS341 - Introduction to Algorithms (Fall 2010)
 - CS840 - Advanced Data Structures (Winter 2010)
- Department of Computer Science, University of Chile
- CC30B - Introduction to the Theory of Computation (Spring 2004)

- CC10A - Introduction to Computer Science (Fall 2003)

- **Instructional Apprentice/Teaching Assistant**

School of Computer Science, University of Waterloo

- CS136 - Elementary Algorithm Design and Data Abstraction (IA: Fall 2009, Winter 2010, Winter 2011)
- CS240 - Data Structures and Data Management (IA: Winter 2009, Spring 2009; TA: Fall 2008)
- CS135 - Designing Functional Programs (IA: Spring 2008)
- CS134 - Principles of Computer Science (IA: Winter 2007, Spring 2007, Fall 2007, Winter 2008; TA: Spring 2006)
- CS430 - Applications Software Engineering (TA: Fall 2006)

- **Teaching Assistant**

School of Engineering and Sciences, University of Chile

- CC30B - Introduction to the Theory of Computation (Spring 2004)
- CC30A - Algorithms and Data Structures (Fall 2004)
- CC10A - Introduction to Computer Science (Fall 2002, Spring 2002, Fall 2003, Fall 2004)
- MA11A - Algebra (Fall 2001, Fall 2002)
- MA12A - Calculus (Fall 2001)

REVIEW ACTIVITIES

- **Conference Program Committee Member**

- 17th Workshop on Advances in Parallel and Distributed Computational Models (APDCM 2015) (IPDPS Workshop).
- Sixth Workshop on Massive Data Algorithmics (MASSIVE 2014)
- 16th Workshop on Advances in Parallel and Distributed Computational Models (APDCM 2014) (IPDPS Workshop).
- XXIV Encuentro Chileno de Computación (ECC 2012) (Chilean Computer Science Meeting)
- XXIII Encuentro Chileno de Computación (ECC 2011)
- XXXVII Conferencia Latinoamericana de Informática (CLEI 2011) (Latin American Informatics Conference)

- **Journal Referee**

- ACM Transactions on Parallel Computing (TOPC)
- Information Processing Letters (IPL)
- ACM Transactions on Algorithms (TACM)
- Discrete Mathematics & Theoretical Computer Science (DMTCS)
- IEEE Transactions on Fuzzy Systems (TFS)

- **Conference Referee**

- 34th Foundations of Software Technology and Theoretical Computer Science (FSTTCS 2014)
- 8th, 9th, 10th, 11th Latin American Theoretical Informatics Symposium (LATIN 2008, 2010, 2012, 2014)

- 8th, 9th, 11th, 12th Workshop on Approximation and Online Algorithms (WAOA 2010, 2011, 2013, 2014)
- 35th and 40th International Colloquium on Automata, Languages and Programming (ICALP 2008, 2013)
- 24th Annual Symposium on Combinatorial Pattern Matching (CPM 2013)
- Meeting on Algorithm Engineering & Experiments (ALENEX 2007, 2013)
- 21st ACM International Conference on Information and Knowledge Management (CIKM 2012)
- 24th Canadian Conference on Computational Geometry (CCCG 2012)
- 20th Annual European Symposium on Algorithms (ESA 2012)
- Workshop for Women in Machine Learning (WiML 2010)
- String Processing and Information Retrieval Symposium (SPIRE 2010)
- 5th Canadian Student Conference on Biomedical Computing (CSCBCE 2010)
- Workshop on Algorithms and Computation (WALCOM 2010)
- 17th International Conference on Advanced Computing and Communications (ADCOM 2009)
- Algorithms and Data Structures Symposium (WADS 2009)
- 8th International Symposium on Experimental Algorithms (SEA 2009)

ADMINISTRATIVE ACTIVITIES AND SERVICE

- **Co-organizer**
25th Canadian Conference on Computational Geometry (CCCG 2013)
- **Vice President** (2010-2011)
Computer Science Graduate Student Association, University of Waterloo
- **Graduate Student Liaison - Distinguished Lecture Series** (2010-2012)
David R. Cheriton School of Computer Science, University of Waterloo
- **Logistics Coordinator, Session Chair**
5th Canadian Student Conference on Biomedical Computing (CSCBCE 2010)
- **Local Organizer, Session Chair**
9th Latin American Theoretical Informatics Symposium (LATIN 2010)
- **Graduate Student Representative** (2009-2010)
School Advisory Committee on Appointments, David R. Cheriton School of Computer Science, University of Waterloo
- **Global Representative for Chile** (2009-2012)
University of Waterloo
- **Co-organizer** (2007-2010)
Algorithms and Complexity Seminar Series, University of Waterloo